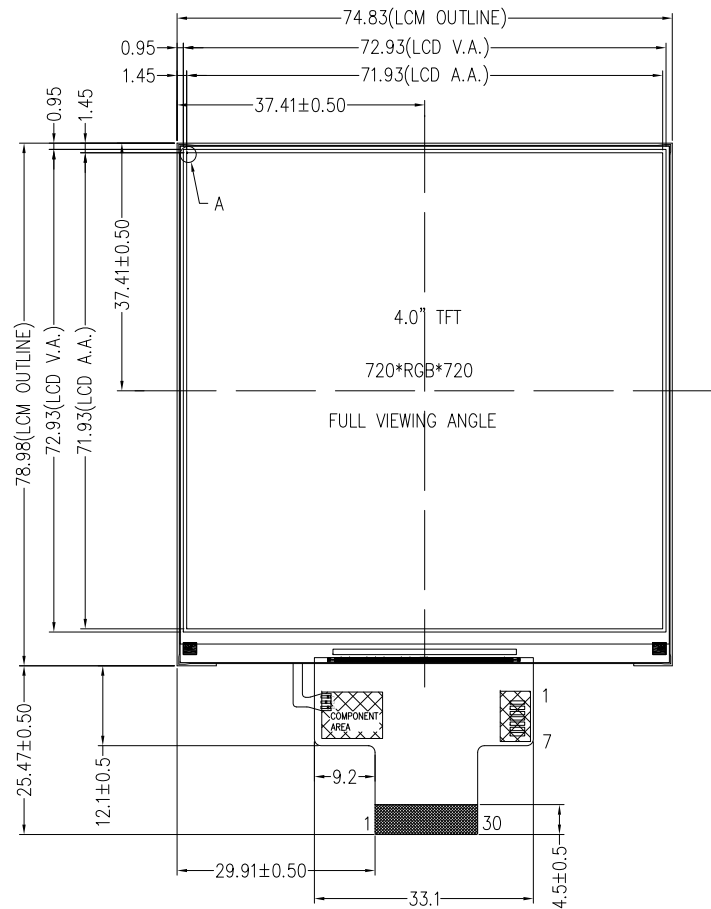
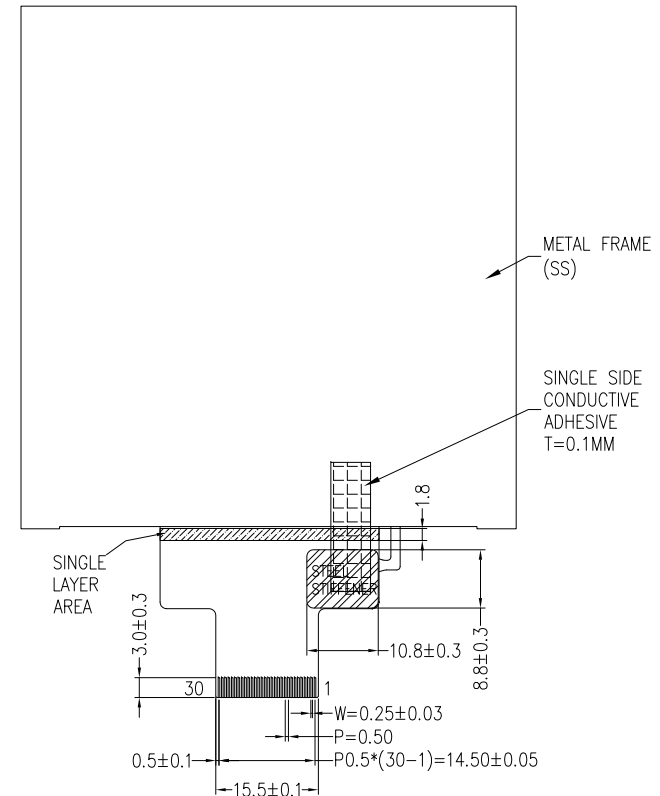


DETAIL A
(SCALE 100:1)



BACKLIGHT CIRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VCI=3.3V (TYP.), IOVCC=1.8V (TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ST7703
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. BACKLIGHT TYPE: WHITE DUAL-DICE LED, IF=40mA, VF=23.2V~27.2V
8. SURFACE LUMINANCE: 660CD/M² (TYP.)
9. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
10. RECOMMEND MATCHING CONNECTOR: HRS FH19SC-30S-0.5SH OR EQUIVALENT
11. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
12. UNMARKED TOLERANCE: ±0.3MM
13. ROHS COMPLIANT

PIN ASSIGNMENT FOR LCM	
PIN	SYMBOL
1	LEDK1
2	LEDK2
3	LEDK2
4	VCI
5	IOVCC
6	RESET
7	TE
8	PWM
9	GND
10	D0P
11	D0N
12	GND
13	D1P
14	D1N
15	GND
16	CLKP
17	CLKN
18	GND
19	D2P
20	D2N
21	GND
22	D3P
23	D3N
24	GND
25	NC(TP_INT)
26	NC(TP_SDA)
27	NC(TP_SCL)
28	NC(TP_RESET)
29	NC(TP_VCI)
30	NC(IOVCC)



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2025.03.05		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0400ST-1
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT MODULE

01	FIRST ISSUE	RITA	2025.03.05
VER.	REVISED DESCRIPTION	REVISER	DATE